

wherein a minimum gap between adjacent LED chips is maintained to provide accurate chip placement and allow for thermal expansion of the ED chip, while maintaining a constant pitch between pixels.

18. (New) The LED array of Claim 17 wherein the constant pitch is approximately 21.2 um.

19. (New) The LED array of Claim 17 wherein the LED array is a 1200 SPI array.

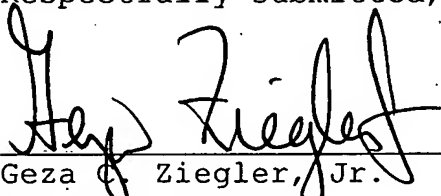
20. (New) The LED array of Claim 17 wherein the minimum gap is approximately 5.9 um.

REMARKS

Claims 17-20 have been added.

The Commissioner is hereby authorized to charge payment for any fees associated with this communication or credit any over payment to Deposit Account No. 24-0037.

Respectfully submitted,


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12/23/02
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